

**Product Summary**

Part #	V <sub>DS</sub>	R <sub>DS(on).typ</sub> (@V <sub>GS</sub> =10V)	R <sub>DS(on).typ</sub> (@V <sub>GS</sub> =4.5V)	I <sub>D</sub>
DP066N04MGL	40V	5.7mΩ	8.8mΩ	54A

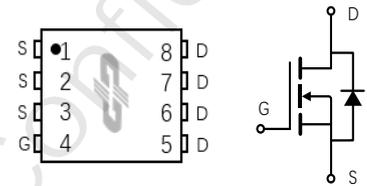
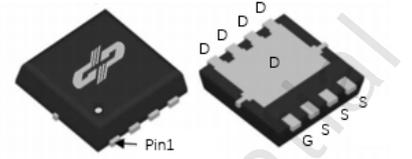
**Features**

- Uses advanced MOSFET-DPMOS2 technology
- Extremely low on-resistance R<sub>DS(on)</sub>
- Excellent Q<sub>g</sub>xR<sub>DS(on)</sub> product(FOM)
- Qualified according to JEDEC criteria

**Applications**

- Motor control and drive
- Battery management
- UPS (Uninterruptible Power Supplies)

DFN 3.3x3.3



100% Avalanche Tested

 100% R<sub>g</sub> Tested

**Package Marking and Ordering Information**

Part #	Marking	Package	Packing
DP066N04MGL	066N04MGL	DFN3.3x3.3	Tape&Reel


**Absolute Maximum Ratings**

Parameter	Symbol	Value	Unit
Drain-source voltage	V <sub>DS</sub>	40	V
Continuous drain current	I <sub>D</sub>	54	A
T <sub>C</sub> = 25°C		34	
T <sub>C</sub> = 100°C			
Pulsed drain current (T <sub>C</sub> = 25°C, t <sub>p</sub> limited by T <sub>jmax</sub> )	I <sub>D pulse</sub>	216	A
Avalanche energy, single pulse (L=0.3mH, R <sub>g</sub> =25Ω) <sup>[1]</sup>	E <sub>AS</sub>	38	mJ
Gate-Source voltage	V <sub>GS</sub>	±20	V
Power dissipation (T <sub>C</sub> = 25°C)	P <sub>tot</sub>	35	W
Operating junction and storage temperature	T <sub>j</sub> , T <sub>stg</sub>	-55...+150	°C

 [1].EAS is tested at starting T<sub>j</sub> = 25°C, V<sub>GS</sub> = 10V.

**Thermal Resistance**

Parameter	Symbol	Max	Unit
Thermal resistance, junction – case.	$R_{thJC}$	3.6	°C/W
Thermal resistance, junction – ambient(min. footprint)	$R_{thJA}$	60	

**Electrical Characteristic (at  $T_j = 25\text{ }^\circ\text{C}$ , unless otherwise specified)**

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

**Static Characteristic**

Drain-source breakdown voltage	$BV_{DSS}$	40	-	-	V	$V_{GS}=0V, I_D=250\mu A$
Gate threshold voltage	$V_{GS(th)}$	1.2	1.7	2.2	V	$V_{DS}=V_{GS}, I_D=250\mu A$
Zero gate voltage drain current	$I_{DSS}$	-	-	1 100	$\mu A$	$V_{DS}=40V, V_{GS}=0V$ $T_j=25\text{ }^\circ\text{C}$ $T_j=150\text{ }^\circ\text{C}$
Gate-source leakage current	$I_{GSS}$	-	-	$\pm 100$	nA	$V_{GS}=\pm 20V, V_{DS}=0V$
Drain-source on-state resistance	$R_{DS(on)}$	-	5.7 8.8	6.8 11	mΩ	$V_{GS}=10V, I_D=20A$ $V_{GS}=4.5V, I_D=16A$
Gate resistance	$R_g$	-	2	5	Ω	$V_{GS}=0V, V_{DS}=0V,$ $f=1\text{MHz}$
Transconductance <sup>[2]</sup>	$g_{fs}$	-	50	-	S	$V_{DS}=25V, I_D=10A$

**Dynamic Characteristic<sup>[2]</sup>**

Input Capacitance	$C_{iss}$	-	152	-	pF	$V_{GS}=0V, V_{DS}=20V,$ $f=1\text{MHz}$
Output Capacitance	$C_{oss}$	-	218	-		
Reverse Transfer Capacitance	$C_{rss}$	-	10	-		
Gate Total Charge	$Q_g$	-	13	-	nC	$V_{GS}=10V, V_{DS}=20V,$ $I_D=8A, f=1\text{MHz}$
Gate-Source charge	$Q_{gs}$	-	3.8	-		
Gate-Drain charge	$Q_{gd}$	-	1.5	-		
Turn-on delay time	$t_{d(on)}$	-	5.5	-	ns	$V_{GS}=10V, V_{DD}=20V,$ $R_{G_{ext}}=2.7\Omega$
Rise time	$t_r$	-	39	-		
Turn-off delay time	$t_{d(off)}$	-	15	-		
Fall time	$t_f$	-	9	-		

**Body Diode Characteristic**

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	$V_{SD}$	-	0.8	1.2	V	$V_{GS}=0V, I_{SD}=20A$
Diode continuous forward current	$I_S$	-	-	54	A	$TC = 25^{\circ}C$
Diode pluse current	$I_{S\ pluse}$	-	-	216	A	$TC = 25^{\circ}C$
Body Diode Reverse Recovery Time <sup>[2]</sup>	$t_{rr}$	-	13	-	ns	$I_F=20A, dI/dt=100A/\mu s$
Body Diode Reverse Recovery Charge <sup>[2]</sup>	$Q_{rr}$	-	4	-	nC	

[2]. Defined by design. Not subject to production test

**Typical Performance Characteristics**

Fig 1: Output Characteristics

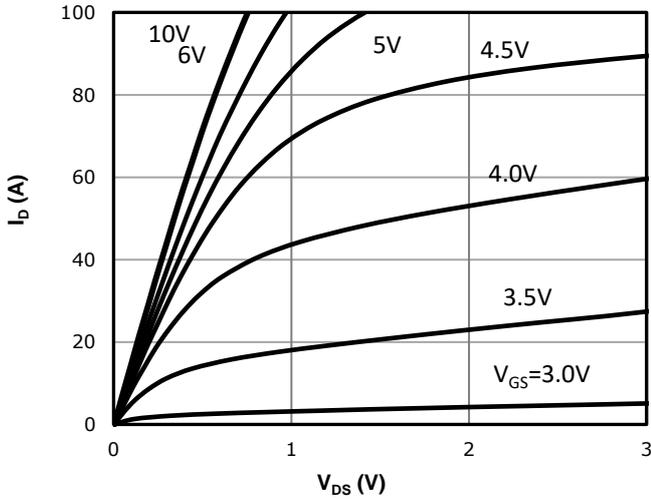


Fig 2: Transfer Characteristics

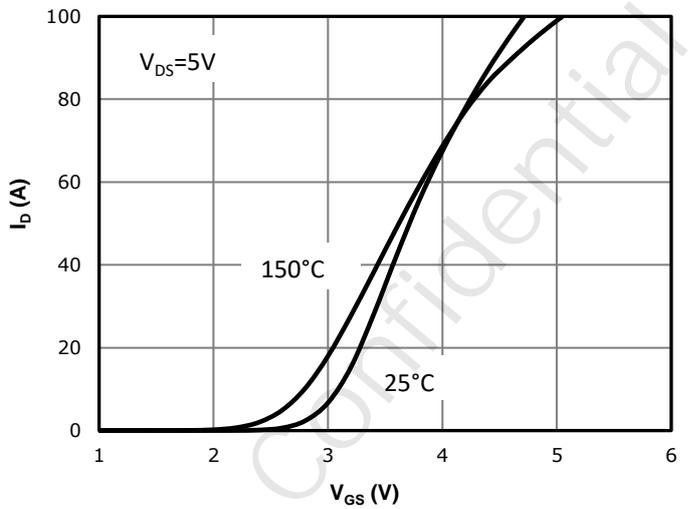


Fig 3:  $R_{DS(on)}$  vs Drain Current and Gate Voltage

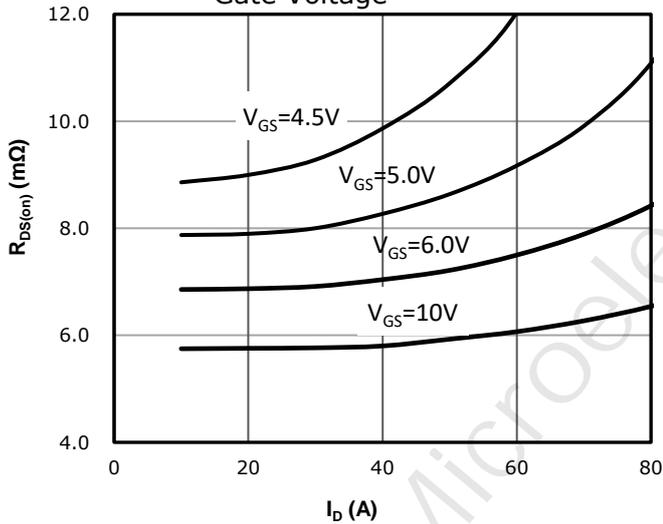


Fig 4:  $R_{DS(on)}$  vs Gate Voltage

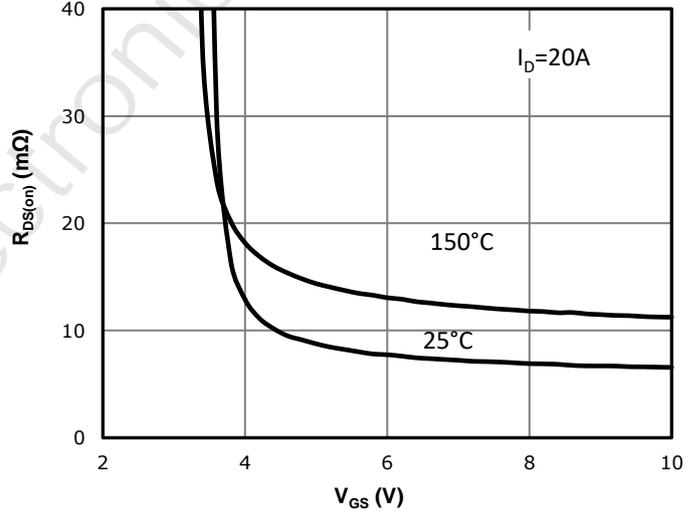


Fig 5:  $R_{DS(on)}$  vs. Temperature

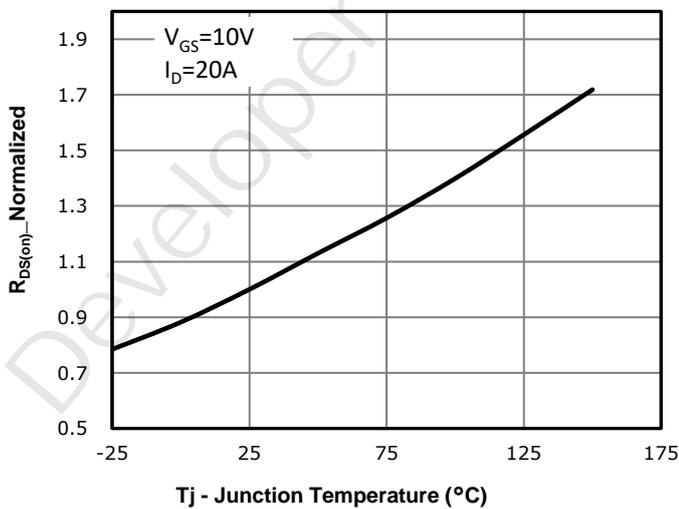


Fig 6: Capacitance Characteristics

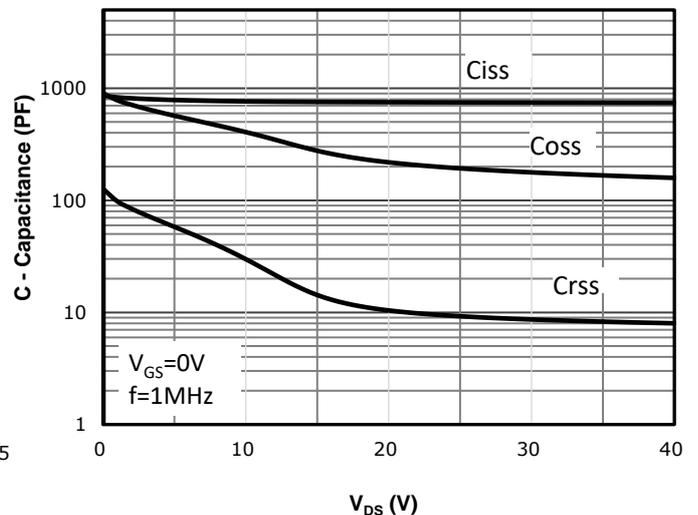


Fig 7: Gate Charge Characteristics

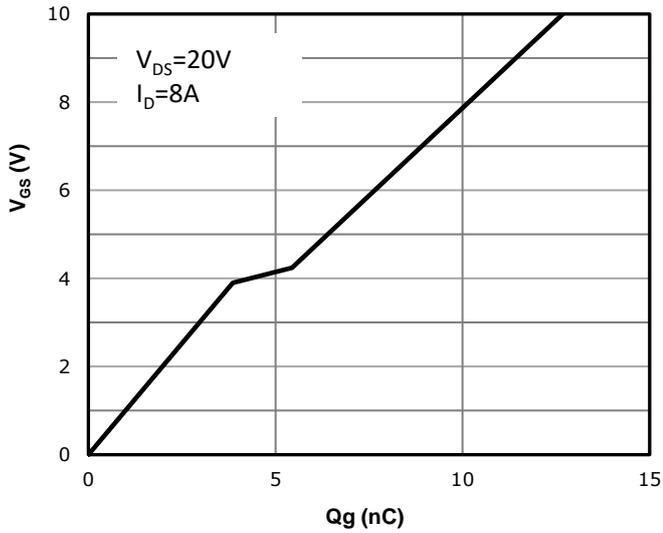


Fig 8: Body-diode Forward Characteristics

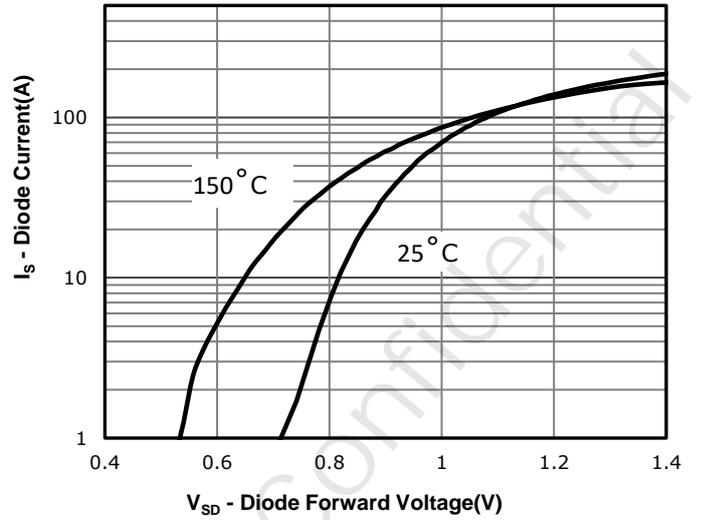


Fig 9: Power Dissipation

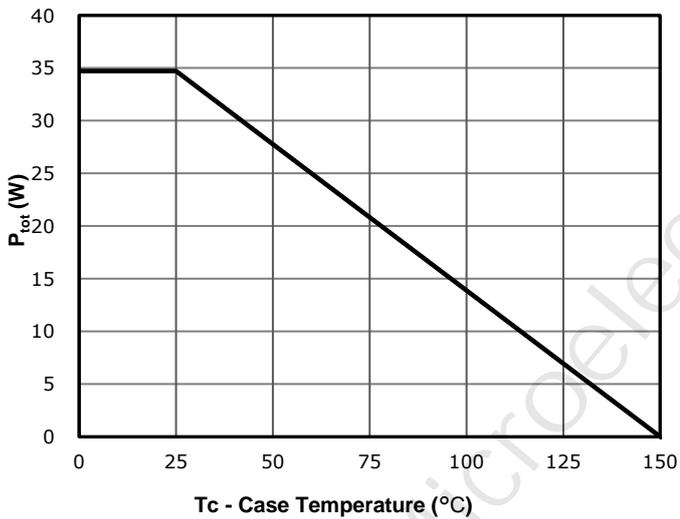


Fig 10: Drain Current Derating

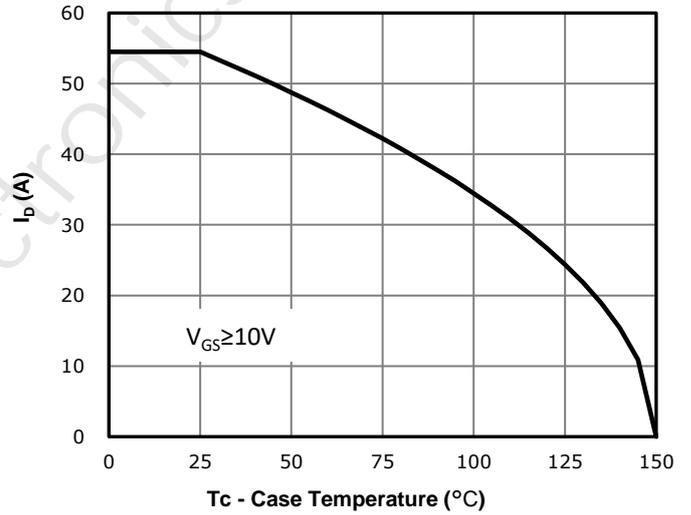


Fig 11: Safe Operating Area

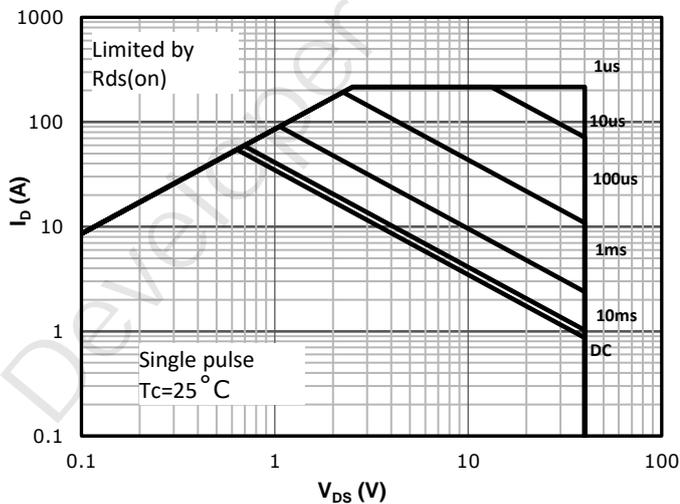
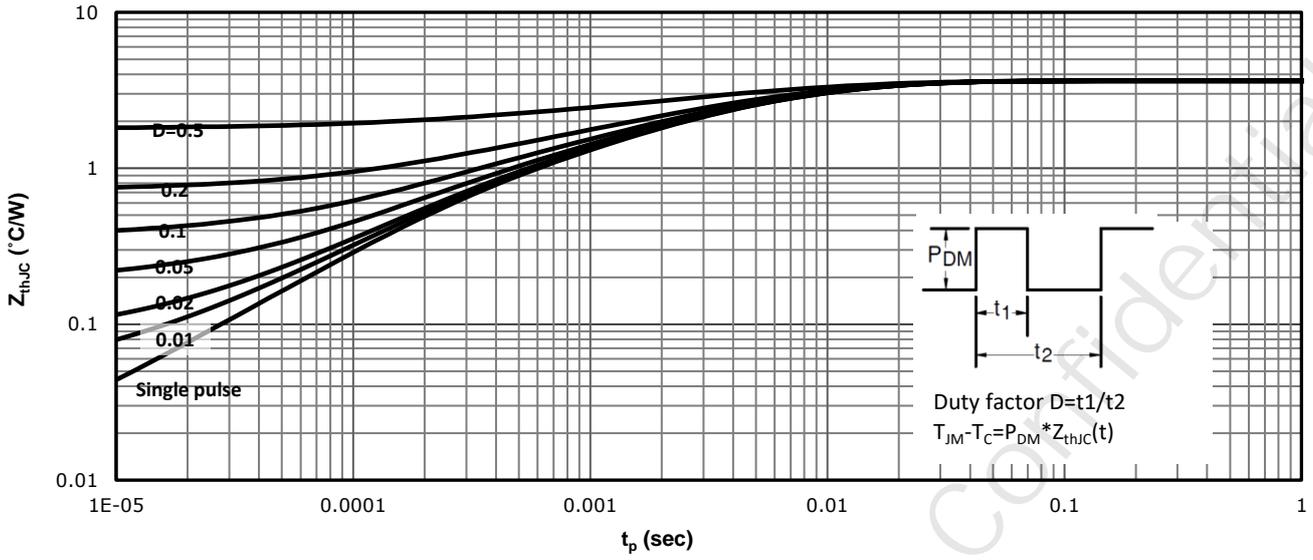
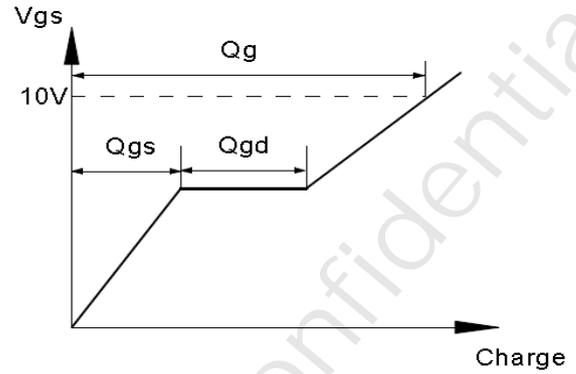
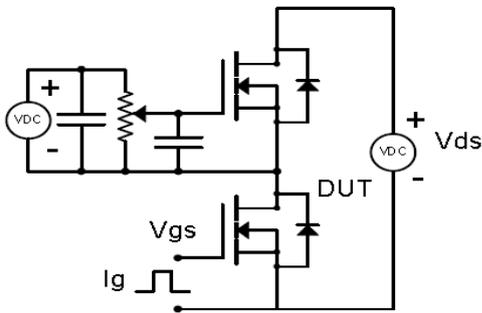


Fig 12: Max. Transient Thermal Impedance

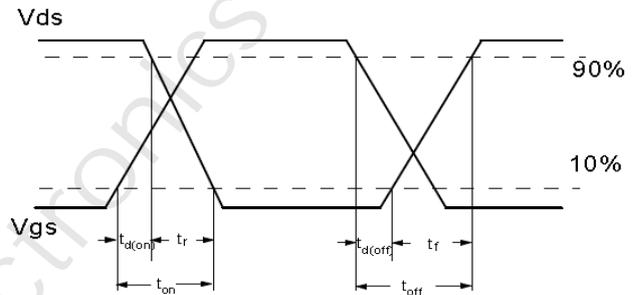
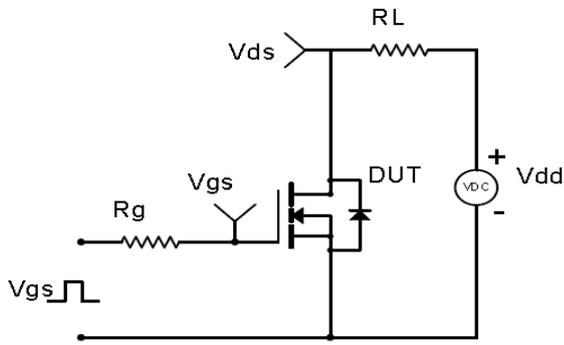


## Test Circuit & Waveform

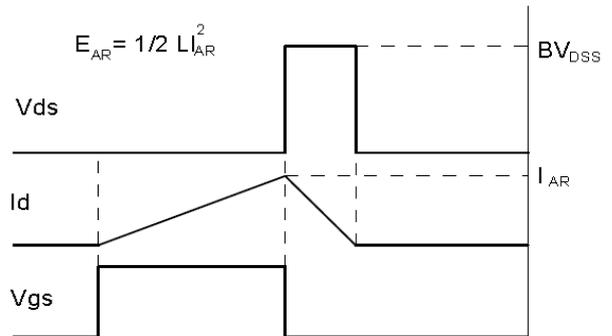
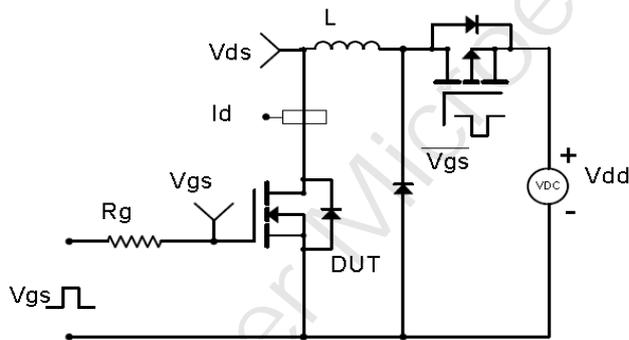
Gate Charge Test Circuit & Waveform



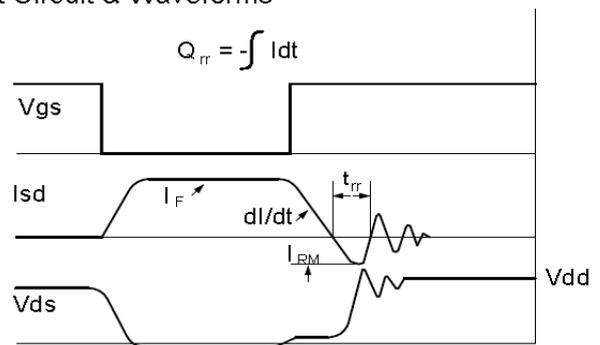
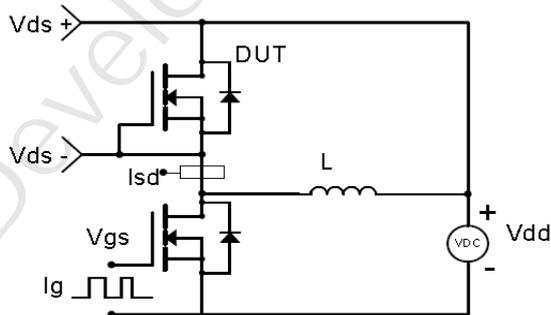
Resistive Switching Test Circuit & Waveforms



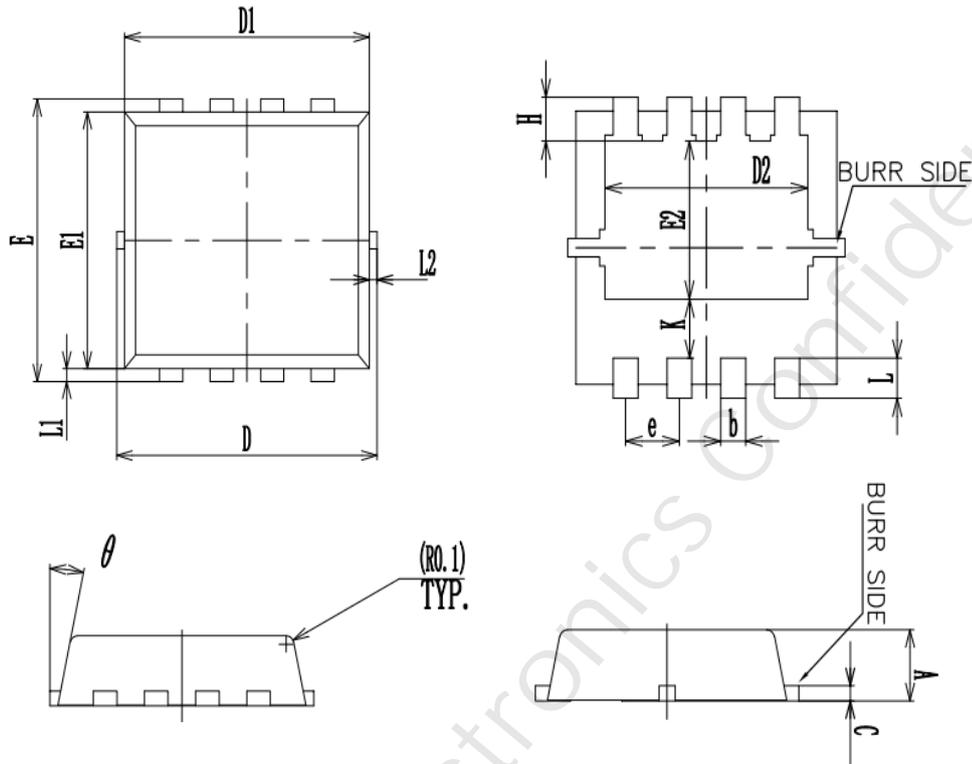
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms

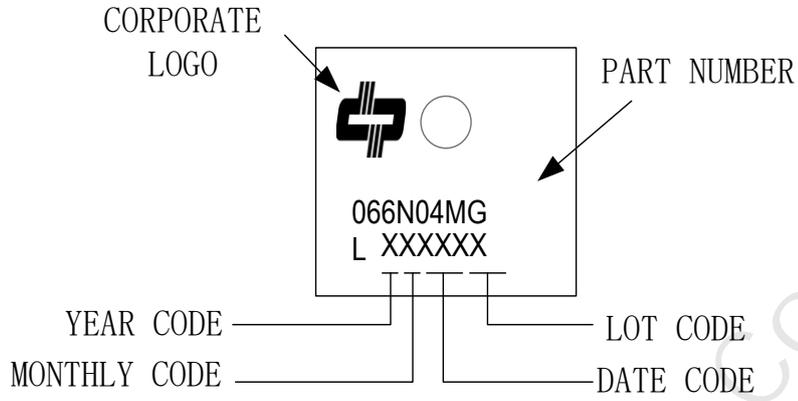


**Package Outline: DFN3.3x3.3**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.70	0.90	0.028	0.035
b	0.25	0.35	0.010	0.014
c	0.14	0.15	0.006	0.006
D	3.15	3.30	0.124	0.130
D1	3.05	3.15	0.120	0.124
D2	2.35	2.45	0.093	0.096
e	0.65 BSC		0.026 BSC	
E	3.20	3.30	0.126	0.130
E1	2.90	3.00	0.114	0.118
E2	1.64	1.74	0.065	0.069
H	0.38	0.48	0.015	0.019
K	0.59	0.69	0.023	-
L	0.25	0.40	0.010	0.016
L1	0.10	0.15	0.004	0.006
L2	-	0.15	-	0.006
θ	8°	12°	-	-

**Part Marking Information**



## Revision History

Revision	Major changes
1.0	Release for formal version

### 重要声明 Important Notice

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