

Product Summary

Part #	V _{DS}	R _{DS(on).typ} (@V _{GS} =10V)	R _{DS(on).typ} (@V _{GS} =4.5V)	I _D
DP050N03FTLB	30V	4.7mΩ	5.8mΩ	60A

Features

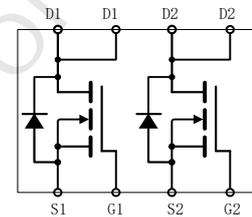
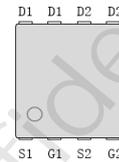
- Advanced high cell density Trench MOSFET technology
- Better R_{DS(on)} enabled by a low R_{DSon.spr} low conduction losses
- Excellent Q_g×R_{DS(on)} product(FOM)
- Qualified according to JEDEC criteria

Applications

- Battery management
- Power Management Switches



Top view


Package Marking and Ordering Information

Part #	Marking	Package	Packing
DP050N03FTLB	050N03FTLB	DFN 5x6-8	Tape/Reel


Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V _{DS}	30	V
Continuous drain current T _C = 25°C T _C = 100°C	I _D	60 38	A
Pulsed drain current (T _C = 25°C, t _p limited by T _{jmax})	I _{D pulse}	240	A
Avalanche energy, single pulse (l=0.3mH, R _g =25) ^[1]	E _{AS}	86	mJ
Gate-Source voltage	V _{GS}	±20	V
Power dissipation (T _C = 25°C)	P _{tot}	22	W
Operating junction and storage temperature	T _j , T _{stg}	-55...+150	°C

[1].EAS is tested at starting T_j = 25°C, V_{GS} = 10V.

Thermal Resistance

Parameter	Symbol	Max	Unit
Thermal resistance, junction – case.	R _{thJC}	6	°C/W
Thermal resistance, junction – ambient(min. footprint)	R _{thJA}	75	

Electrical Characteristic (at T_j = 25 °C, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Static Characteristic						
Drain-source breakdown voltage	BV _{DSS}	30	-	-	V	V _{GS} =0V, I _D =250uA
Gate threshold voltage	V _{GS(th)}	1.2	-	2	V	V _{DS} =V _{GS} , I _D =250uA
Zero gate voltage drain current	I _{DSS}	-	-	1	μA	V _{DS} =30V, V _{GS} =0V T _j =25°C T _j =150°C
		-	-	100		
Gate-source leakage current	I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
Drain-source on-state resistance	R _{DS(on)}	-	4.7	5.6	mΩ	T _j =25°C V _{GS} =10V, I _D =15A
		-	5.8	7.6	mΩ	V _{GS} =4.5V, I _D =12A
Gate resistance	R _g	-	1.7	5	Ω	V _{GS} =0V, V _{DS} =0V, f=1MHz
Transconductance ^[2]	g _{fs}	-	90	-	S	V _{DS} =5V, I _D =40A

Dynamic Characteristic^[2]

Input Capacitance	C _{iss}	-	2278	-	pF	V _{GS} =0V, V _{DS} =15V, f=1MHz
Output Capacitance	C _{oss}	-	284	-		
Reverse Transfer Capacitance	C _{rss}	-	270	-		
Gate Total Charge(V _{GS} =10V)	Q _g	-	54	-	nC	V _{GS} =10V, V _{DS} =15V, I _D =20A, f=1MHz
Gate Total Charge(V _{GS} =4.5V)	Q _g	-	27	-		
Gate-Source charge	Q _{gs}	-	9.7	-		
Gate-Drain charge	Q _{gd}	-	11	-		
Turn-on delay time	t _{d(on)}	-	5.7	-	ns	V _{GS} =10V, V _{DD} =15V, R _{G_ext} =2.7Ω
Rise time	t _r	-	30	-		
Turn-off delay time	t _{d(off)}	-	38	-		
Fall time	t _f	-	16	-		

Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	-	-	1.2	V	$V_{GS}=0V, I_{SD}=20A$
Diode continuous forward current	I_s	-	-	60	A	TC = 25°C
Diode pluse current	$I_{s\ pluse}$	-	-	240	A	TC = 25°C
Body Diode Reverse Recovery Time ^[2]	t_{rr}	-	12	-	ns	$I_F=20A, dI/dt=100A/\mu s$
Body Diode Reverse Recovery Charge ^[2]	Q_{rr}	-	2.4	-	nC	

[2]. Defined by design. Not subject to production test

Typical Performance Characteristics

Fig 1: Output Characteristics

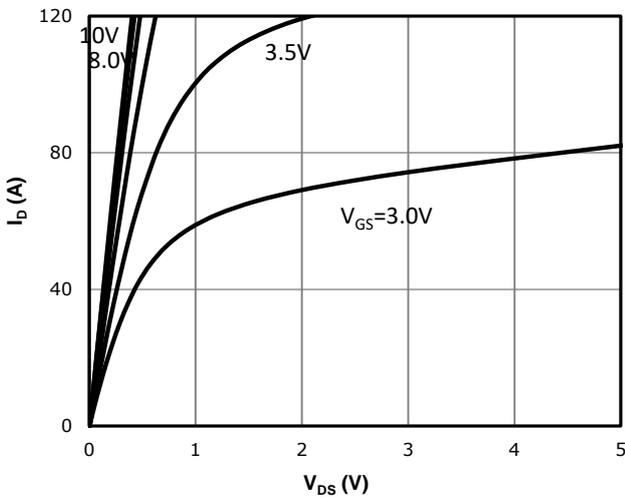


Fig 2: Transfer Characteristics

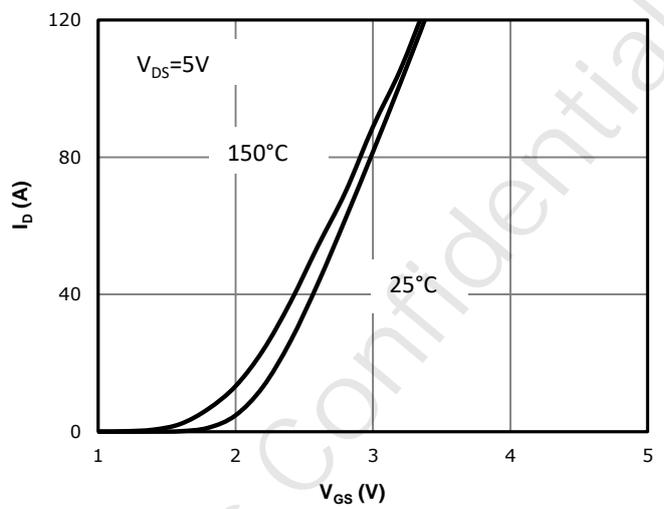


Fig 3: $R_{DS(on)}$ vs Drain Current and Gate Voltage

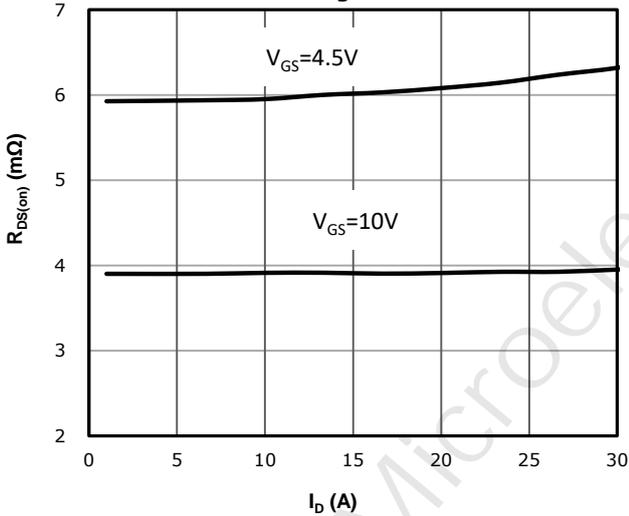


Fig 4: $R_{DS(on)}$ vs Gate Voltage

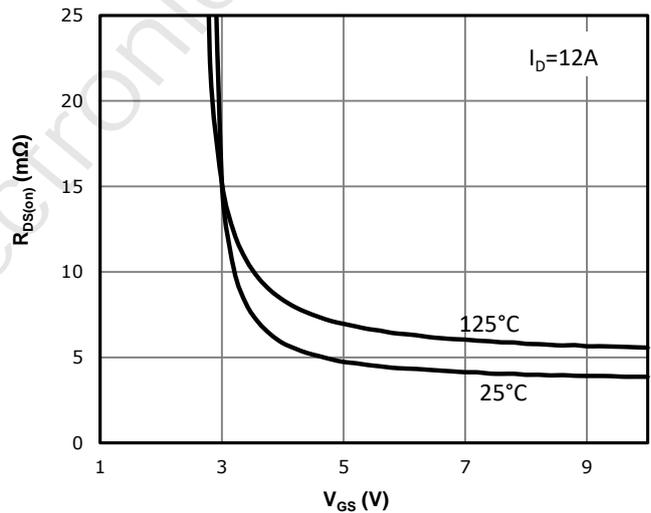


Fig 5: $R_{DS(on)}$ vs. Temperature

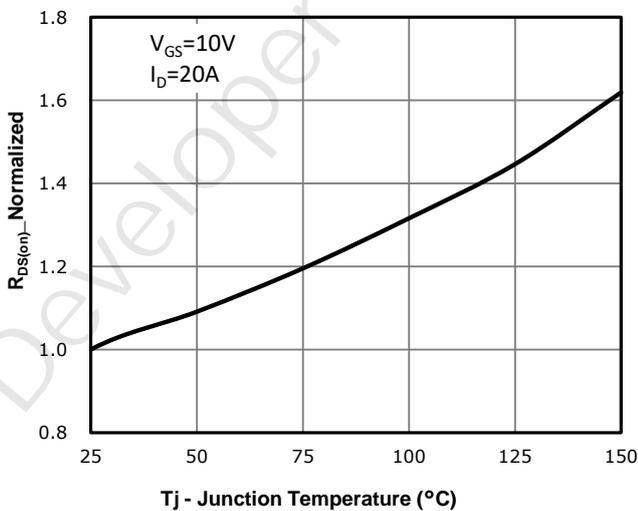


Fig 6: Capacitance Characteristics

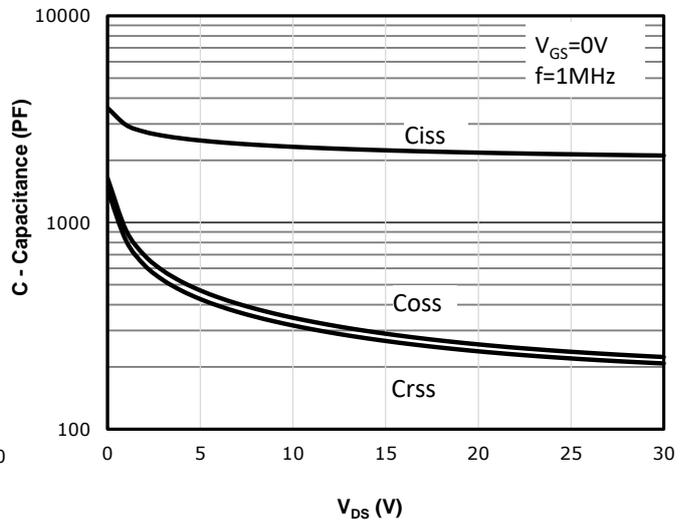


Fig 7: Gate Charge Characteristics

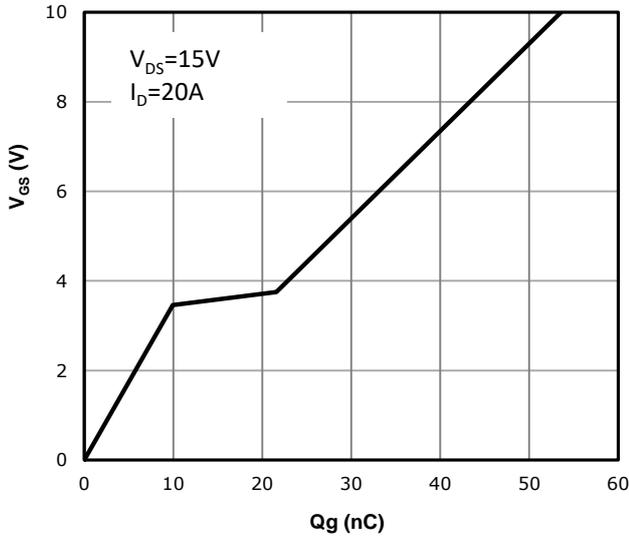


Fig 8: Body-diode Forward Characteristics

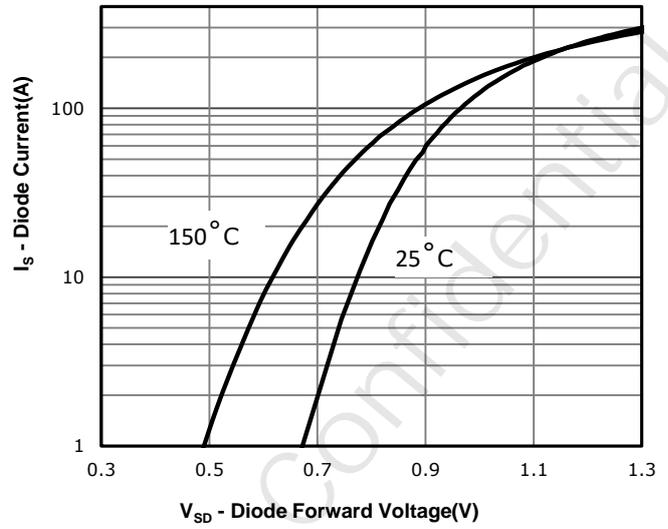


Fig 9: Power Dissipation

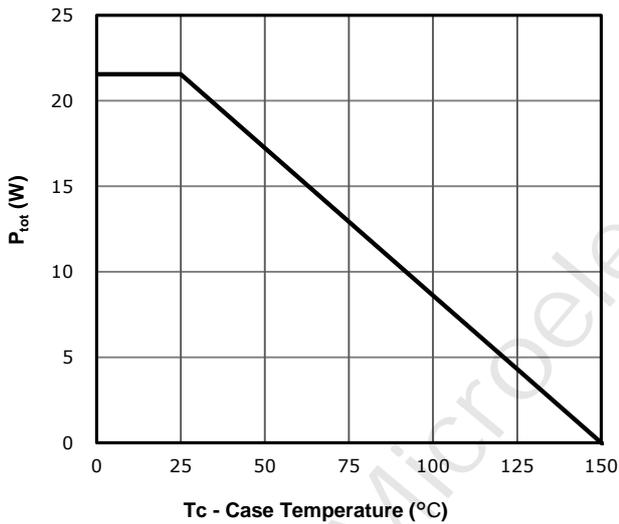


Fig 10: Drain Current Derating

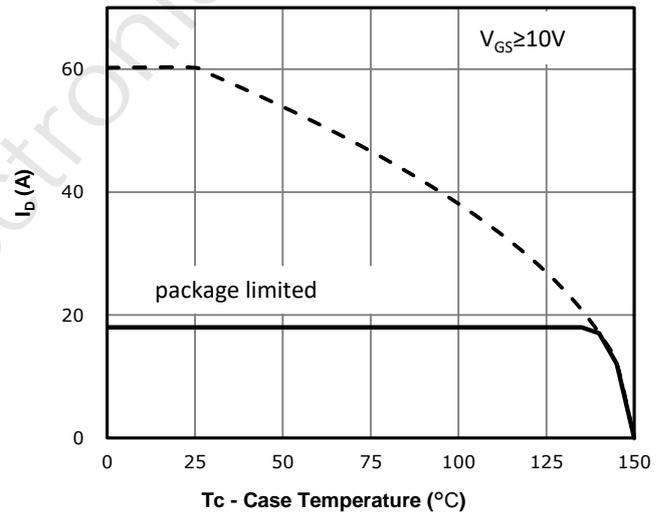


Fig 11: Safe Operating Area

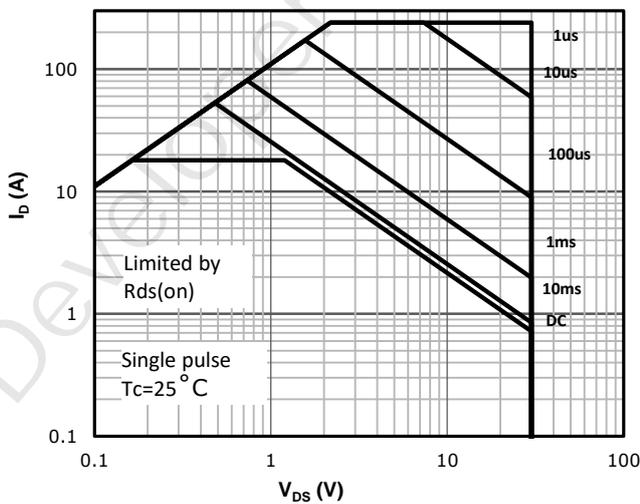
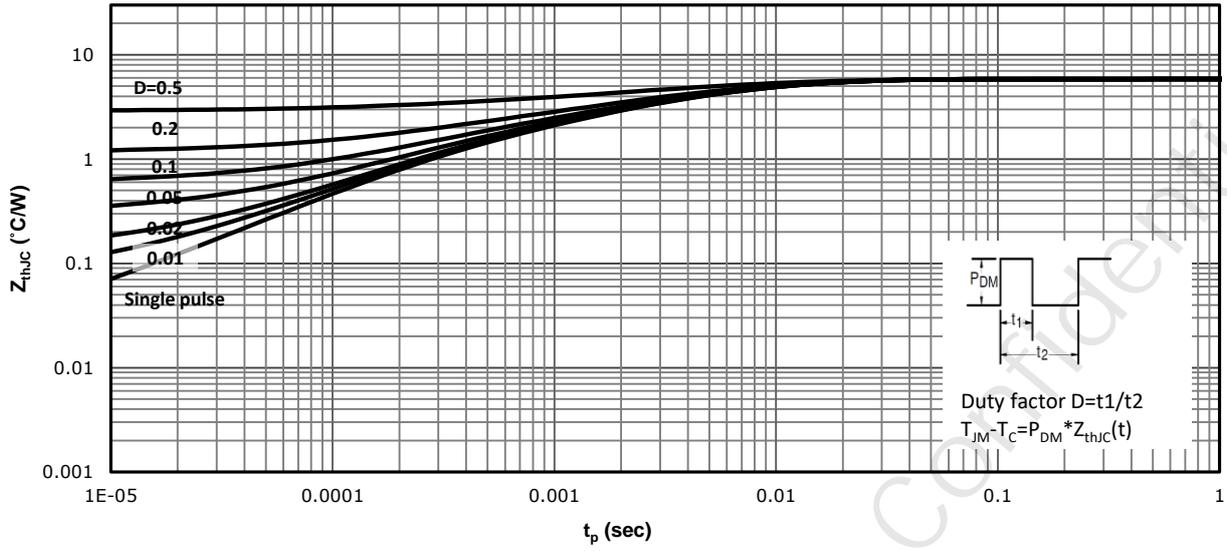
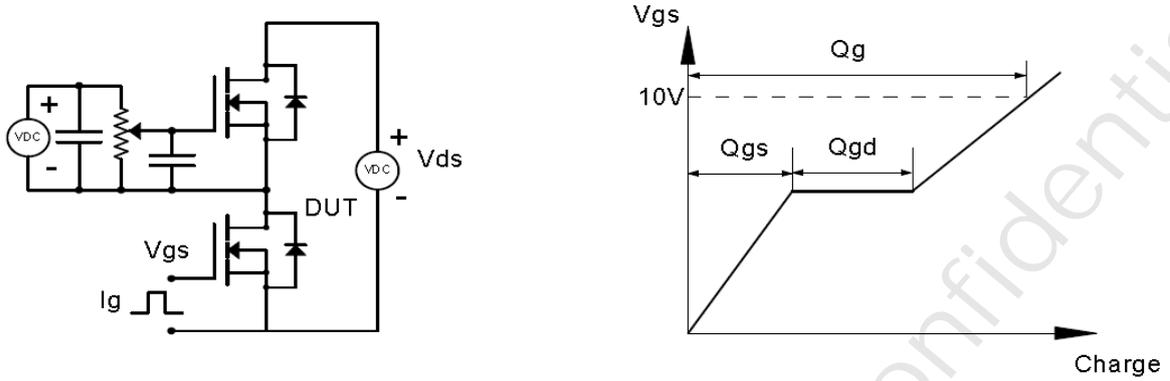


Fig 12: Max. Transient Thermal Impedance

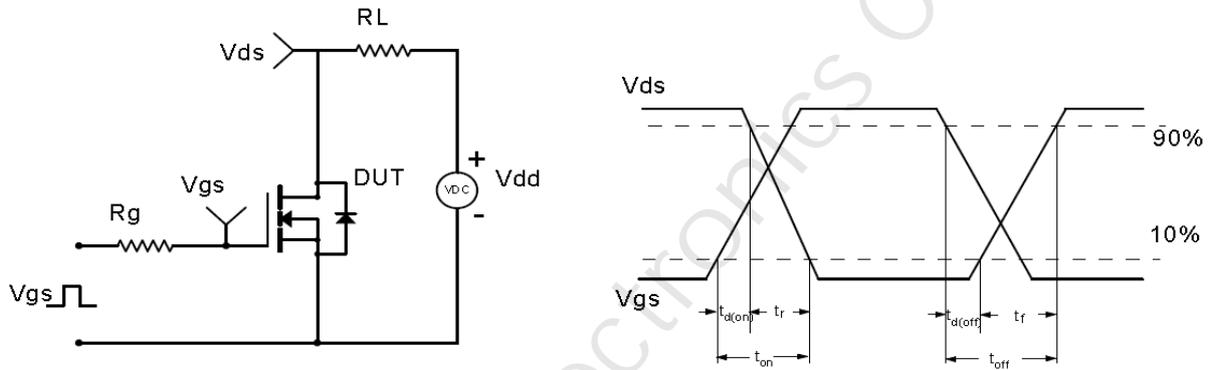


Test Circuit & Waveform

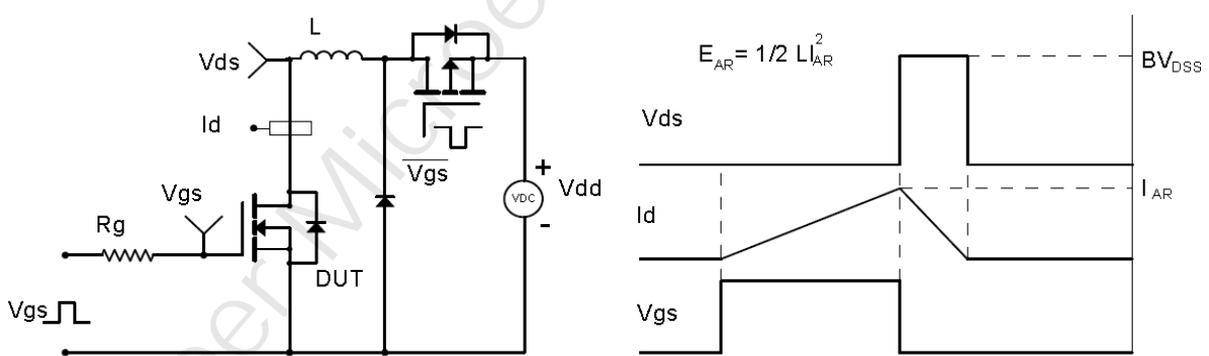
Gate Charge Test Circuit & Waveform



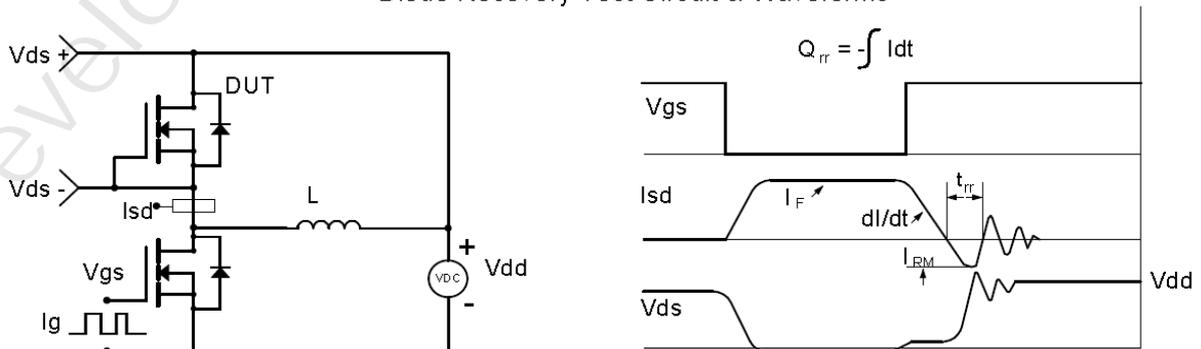
Resistive Switching Test Circuit & Waveforms

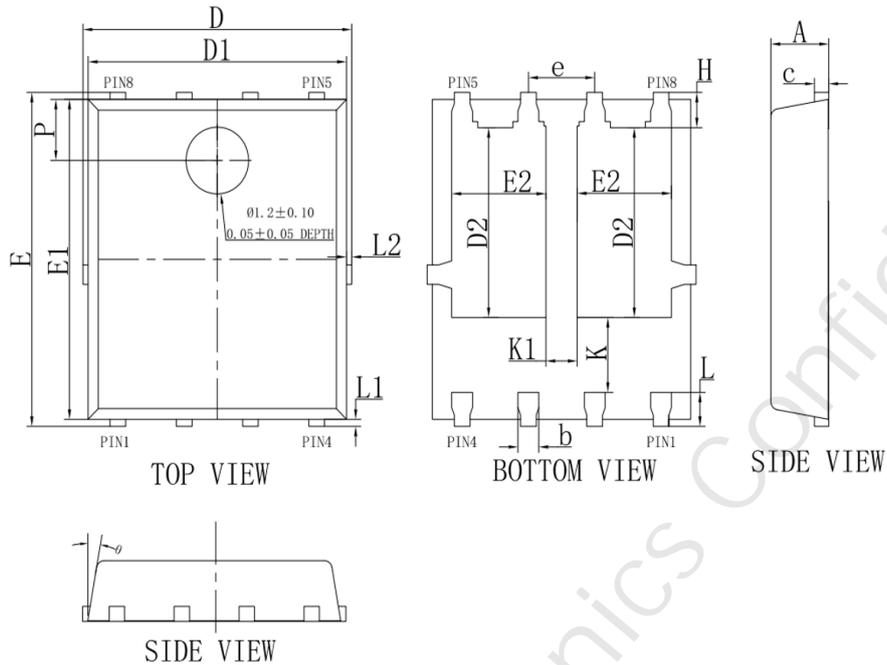


Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



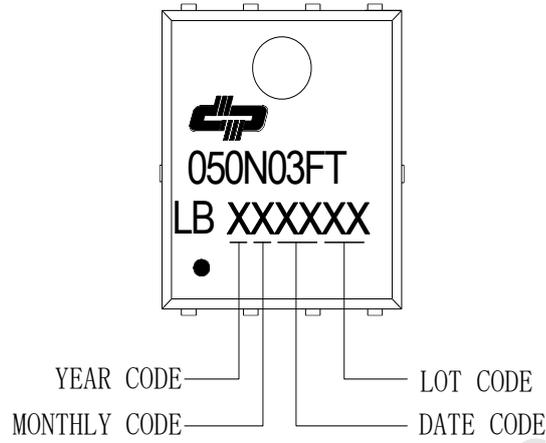
Diode Recovery Test Circuit & Waveforms



Package Outline: PDFN5X6-8


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.00	1.20	0.039	0.047
D	-	5.10	-	0.201
D1	4.90	5.00	0.193	0.197
D2	3.31	3.51	0.130	0.138
E	5.90	6.10	0.232	0.240
E1	5.70	5.80	0.224	0.228
E2	1.71	1.91	0.067	0.075
H	0.51	0.71	0.020	0.028
b	0.35	0.45	0.014	0.018
c	0.21	0.34	0.008	0.013
e	1.17	1.37	0.046	0.054
K	1.25	1.45	0.049	0.057
K1	0.50	0.70	0.020	0.028
L2	0.51	0.71	0.020	0.028
L1	0.06	0.20	0.002	0.008
L2	-	0.15	-	0.006
P	1.00	1.20	0.039	0.047
θ	8°	12°	-	-

Part Marking Information



Revision History

Revision	Major changes
1.1	Release for formal version

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